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O.I.P.E.	PATENT [
SCANNED O.A. A. G.	•

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/832630	DF	257	276	翌月7	Cum

Junko Kohno Kazunori Asano

Semiconductor device and method for manufacturing same

TIME

PTO-2040 12/99

ISSUING CLASSIFICATION								
ORIGINAL			CROSS REFERENCE(S)					
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL	CLASSIFICATION		Continued on Issue Slip Inside File Jacket					

TERMINAL		DRAWINGS		CLAIMS ALLOWED		
L DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent				NOTICE OF AL	LOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
	(Primary Examiner) (Date)					
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this patent have been disclaimed.				•		
	(Legal Instruments Examiner) (Date)			<u> </u>		

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